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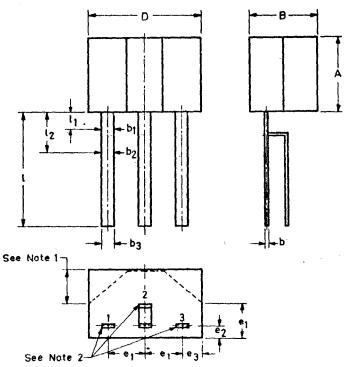




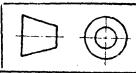
Indian Standard

DIMENSIONS OF SEMICONDUCTOR DEVICES DEVICE OUTLINE ODIO

1. Dimensions—This drawing has been prepared in accordance with IS: 5001-1969 'Guide for preparation of drawings of semiconductor devices'.



Reference'	Millimetres			Notes
	Min	Nom	Max	Hotes
A	-		5-1	
8			4.6	-
ь		0.2		-
b _i	our est		-	4
b ₁	118.40			4
b ₃	_			4 ·
D			7.6	_
e,		2.54		
e ₂		_	1-1	
e,	. —	_	1.3	
1,				4
l,		_		4
•	12.7			



Adopted 4 February 1971

@ August 1971, ISI

Gr 1

IS:5000 (ODIO) - 1971

- Note ! The contour of the outline in this zone is not defined.
- Note 2 In Type A configuration, terminal 1 is base, terminal 2 is emitter and terminal 3 is collector.
- In Type B configuration, terminal 1 is emitter, terminal 2 is base and terminal 3 is collector.
- Note 3 The lead provided should also be capable of being fitted into printed boards having specified hole diameters.
- Note 4 Dimensions b_1 , b_2 and b_3 should be indicated at the end of length l_1 , l_2 and l to take care of stepped, tapered dimensions of the lead.

These dimensions should be specified by the manufacturer depending on the lead configuration and the hole diameter of printed board specified (see Note 3).

- 2. Rules for Coding—See 8.1 of 15:5001-1969.
- 3. Equivalent Designation Code Followed by Other Countries and Organizations (Under consideration).